

<b>Form PTO-1449</b>  <b>U.S. DEPARTMENT OF COMMERCE</b> <b>PATENT AND TRADEMARK OFFICE</b>  <b>INFORMATION DISCLOSURE STATEMENT</b> <b>BY APPLICANT</b>  (Use several sheets if necessary)	<b>ATTY. DKT. NO.</b> 503.28546CV9	<b>SERIAL NO.</b> 09/917912
	<b>APPLICANT</b> TORII, et al.	
	<b>FILING DATE</b> July 31, 2001	<b>GROUP</b> 1756

## U.S. PATENT DOCUMENTS

Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date
AA	5,200,017	4-93	Kawasaki, et al.	156	345	
AB	5,007,981	4-91	Kawasaki, et al.	156	643.1	
AC	4,816,098	3-89	Davis, et al.	156	345	
AD	5,252,881	10-93	Muller, et al.	310	309	
AE	4,481,070	11-84	Thomas, et al.	156	643.1	
AF	4,428,796	1-84	Milgram	430	314	
AG	4,722,355	2-88	Moe, et al.	134	73	
AH	4,855,252	8-89	Peterman, et al.	437	189	
AI	4,487,678	12-11-84	Noguchi, et al.	204	298	
AJ						
AK						
AL						

## FOREIGN PATENT DOCUMENTS

Examiner Initial	Document Number	Date	Country	Class	Subclass	Translation/Abstract	
						Yes	No
AM	247,603	5-27-87	Europe ✓				
AN	63-9121	1-88	Japan				
AO	60-145625	1-85	Japan				
AP	57-2585	7-1-82	Japan				
AQ	0187249	11-29-85	Europe				
AR	0219826	10-17-85	Europe				
AS	84/01084	3-15-84	WO				
AT							

## OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

AU	Elliot, David J., "Integrated Circuit Fabrication Technology" 1982, pp. 57-59, 256-257, 267, 270-275.
AV	Endo JP 62-150351 (abstract only)
AW	Patent Abstracts of Japan, JP-A-55 072040, vol. 004, no. 117, 8-20-80.
AX	Database WPI, Derwent Publications Ltd. JP-A-63 157870, 6-30-88.
AY	Database WPI, Derwent Publications Ltd. JP-A-63 204726, 8-24-88.
AZ	
Examiner	Date Considered